

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Yoshihisa MATSUBARA, et al.	Examiner:	Unassigned
Serial No.:	Unassigned	Group Art Unit:	Unassigned
Filed:	Herewith	Docket:	16782
For:	SEMICONDUCTOR APPARATUS OF WHICH RELIABILITY OF INTERCONNECTIONS IS IMPROVED AND MANUFACTURING METHOD OF THE SAME		
	Dated:	June 27, 2003	

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Publication No. 2000-77413, dated March 14, 2000.

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Commissioner for Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on June 27, 2003.

Dated: June 27, 2003

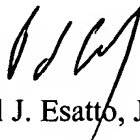


Paul J. Esatto, Jr.

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'P. Esatto, Jr.', is positioned above the printed name.

Paul J. Esatto, Jr., Reg. No. 30,749

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